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**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10006924	FILING DATE 12/04/2001	CLASS 514	SUBCLASS 321	GAU 2817	EXAMINER Jacobs William
**APPLICANTS: Stockstad Troy, 330 / 255		Nguyen, Khanh 2/21/2000 07/515961 6353,363			
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CON OF 09/515,961 03/01/2000 PAT 6,316,469 *					
(*)Data provided by applicant is not consistent with PTO records.					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no		60178-300402	
Verified and Acknowledged Examiners's initials					
TITLE : Low voltage rail-to-rail CMOS output stage					
U.S.DEP'T. OF COMM./PAT. & TM-PTO-438L (Rev. 12-94)					

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